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# Monte Carlo Simulation for the Measurement of Critical Dimension by Scanning Electron Microscopy with the Model-Based Library Method

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**EXTENDED ABSTRACT:** For accurate measurement of semiconductor linewidth, the application of the model-based library (MBL) method in the measurement of critical dimensions (CD) of semiconductor linewidth was investigated in depth based on the electron-solid interaction theory and the transport mechanism of electrons in solids using a Monte Carlo simulation method combined with a neural network algorithm. The effect of various geometrical parameters (such as top width, edge wall angle and height) and different electron beam incidence conditions on the signal profile of the secondary electron line-scan is investigated by constructing a trapezoidal-like sample structure using the finite element mesh method according to ISO 21466[1] with Si as the sample. To further extend the calculations for insulator materials, the emission characteristics of secondary electrons were investigated for the insulator material SiO<sub>2</sub>. The intrinsic secondary electron yield of SiO<sub>2</sub>, as well as the emission energy, excitation depth and emission angle of secondary electrons are investigated using Monte Carlo method calculations. A new dielectric function model (LL model) [2] is introduced to describe the inelastic scattering of electrons using the dielectric function approach in the calculations, and the phonon scattering mechanism is considered. The calculation results show that the calculated results of the secondary electron yield are in excellent agreement with the experimental values and also enrich the database, thus further extending the generality of the numerical method.

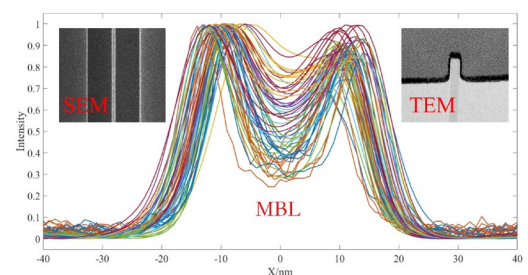
**Keywords :** Monte Carlo simulation; second electron; critical dimension; Model-based library; neural network

## REFERENCES

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## BIOGRAPHY

Guo Peng, male, 30 years old, is a doctoral student in the Department of Modern Mechanics of the University of Science and Technology of China.



**Figure 1.** SE linescans of the SiO<sub>2</sub>/Si gate line structure with different structure parameters

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